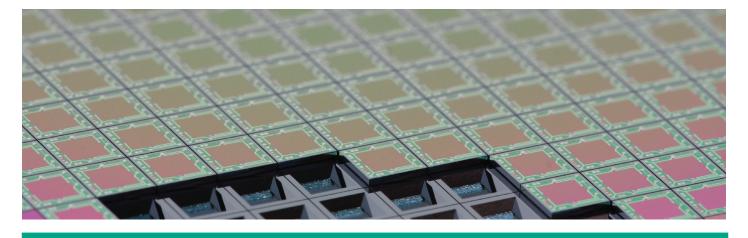
# DIRECT BONDING USING CHEMICAL REACTIVE PLASMA DISCHARGE



#### Contact

# Fraunhofer Institute for Electronic Nano Systems ENAS Technologie-Campus 3 09126 Chemnitz | Germany

#### **Contact person**

Dr. Maik Wiemer Phone: +49 371 45001-233 E-mail: maik.wiemer@enas.fraunhofer.de

Dirk Wuensch Phone: +49 371 45001-262 E-mail: dirk.wuensch@enas.fraunhofer.de

#### Description

Joining two substrates without intermediate layer, the surface roughness is very important. While small variations in the surface roughness can be easily compensated by thick intermediate layers such as glass paste or epoxy resins, the atomic contact is crucial to form a strong and stable bond based on techniques without intermediate layers. For instance, anodic bonding is carried out with surface roughnesses R<sub>a</sub> below 1 nm. Other technologies require special pretreatments, like plasma activation with special gases or a wet chemical treatment, in order to create a smooth hydrophilic or hydrophobic surface.

Besides a wet-chemical pretreatment of the wafers, a chemical-reactive plasma discharge prior to bonding increases the mechanical strength of the direct bonded stack. The plasma activation can be applied to the whole substrate or limited to small areas and structures atop the wafer. Even at curing temperatures of 200 °C, the resulting bonding strength is comparable to high-temperature bonds. The pretreatment with plasma followed by low-temperature bonding can be used for joining material with different coefficients of thermal expansion. It therefore enables the integration of new materials and heterogeneous materials like lithium tantalite (LiTaO<sub>3</sub>), stainless steel or foturan glass.

# Advantages of this bonding technology

- Short bonding time
- High bonding strength
- No additional intermediate layer
- Independent on bonding environment, such as vacuum and nitrogen







Foturan glass		<del>40 30 30 30 30 30 30 30 3</del> 0 <del>3</del> 0 <del>30</del> <del>30</del>
Silicon 100µm	*******	
	*#####################################	
	Silicon	
	 LiTaO <sub>3</sub>	
	Silicon	100µm

### Successfully bonded material combination on wafer-level

	Silicon	Borosilicate glass	Foturan glass	Quartz glass	LiTaO <sub>3</sub>	LTCC (ceramic)	Stainless steel
Silicon	х	х	х	х	х	х	
Borosilicate glass	x	x	x	x	x		x
Foturan glass	х	х	х	х	х		
Quartz glass	х	x	х	х	х		
LiTaO <sub>3</sub>	х	х	х	х	х		
LTCC (ceramic)	x						
Stainless steel		х					

Figures:

page 1: pressure sensor in cooperation with EPCOS AG after Silicon Fusion bonding using chemical reactive plasma discharge. page 2: Silicon-foturan wafer level stack including through holes in foturan glass (SEM cross-section) (left); Silicon-LiTaO<sub>3</sub>-silicon wafer level stack including silicon interposer (SEM cross-section) (right).

Photo acknowledgements: Fraunhofer ENAS All information contained in this datasheet is preliminary and subject to change. Furthermore, the described systems, materials and processes are not commercial products.





