

Chemnitzer Seminar – Materials and Nanotechnologies for MEMS Packaging'18

Advances in High Vacuum Encapsulation for MEMS Packaging based on Metallic Bonding Interfaces

Bernhard REBHAN, Senior Scientist Wafer Bonding

Product Categories

EVG is a global supplier of:

- Wafer Bonders
- Temporary Bonders / Debonders
- Aligners
- Coaters / Developers
- Cleaners
- Inspection / Metrology Systems

EV Group holds the dominant share of the market for wafer bonding equipment and is a technology leader in lithography for advanced packaging and nanotechnology.



Markets and Typical End Products

**Advanced Packaging
3D Interconnect**



CMOS Image Sensors

**Compound Semiconductor
Silicon-Based Power Devices**



LEDs

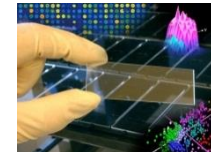
MEMS

(Micro Electro Mechanical Systems)



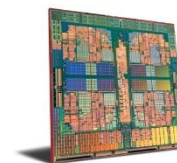
Motion sensors

Nanotechnology



DNA chips / micro labs

**SOI (Silicon On Insulator)
Engineered Substrates**



Micro processor wafers

Global Presence



- Fully owned subsidiaries
- Representatives



EVG North America



EVG Japan



EVG Taiwan



EVG Korea



EVG China

Advances in High Vacuum Encapsulation for MEMS Packaging based on Metallic Bonding Interfaces



- Background and Motivation
- Process Equipment
- Process Results
- Summary & Conclusions

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Background and Motivation

EVG's Wafer Bonding Equipment Portfolio



Pressure:
 $p = 1 \text{ atm}$



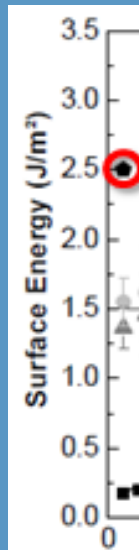
Vacuum level:
 $p > 10^{-6} \text{ mbar}$



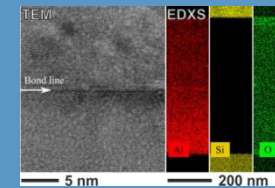
Vacuum level:
 $p < 2 \times 10^{-8} \text{ mbar}$

- EVG[®]580 ComBond[®] (*in-situ* high vacuum) system for
 - Engineered substrates by wafer bonding
 - High vacuum encapsulation by wafer bonding

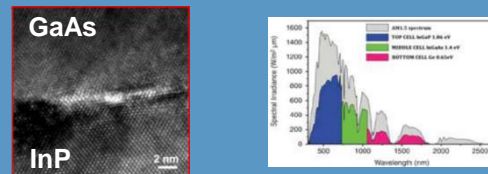
Covalent Bonding



Oxide-free Al-Al wafer bonding



Heterogeneous Material Integration

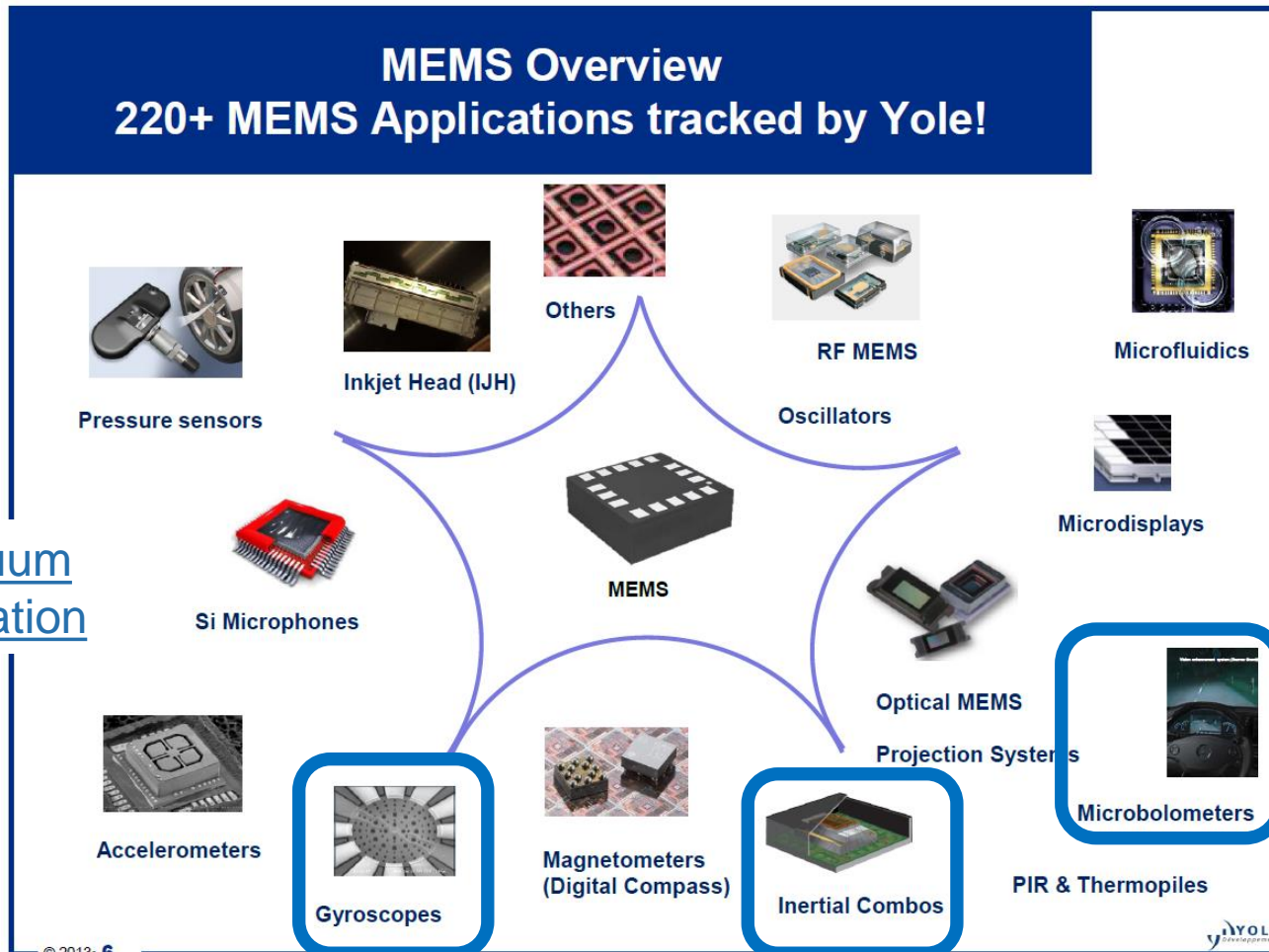


Vacuum Encapsulation



MEMS Overview 220+ MEMS Applications tracked by Yole!

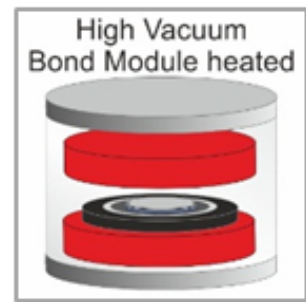
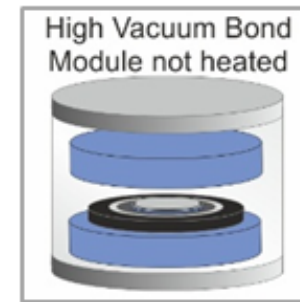
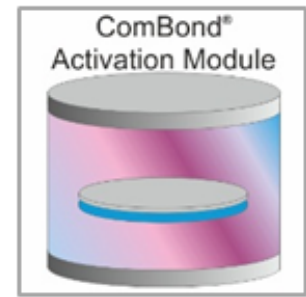
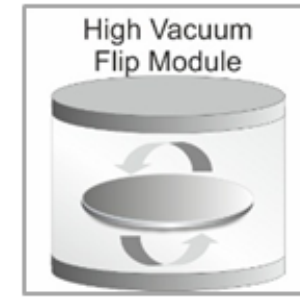
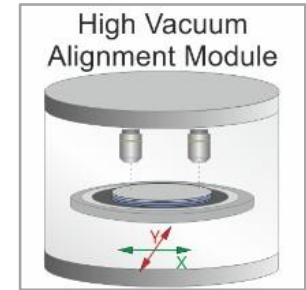
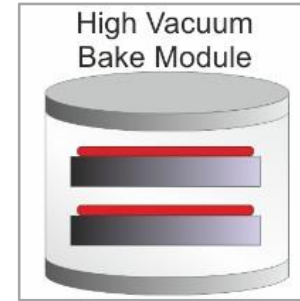
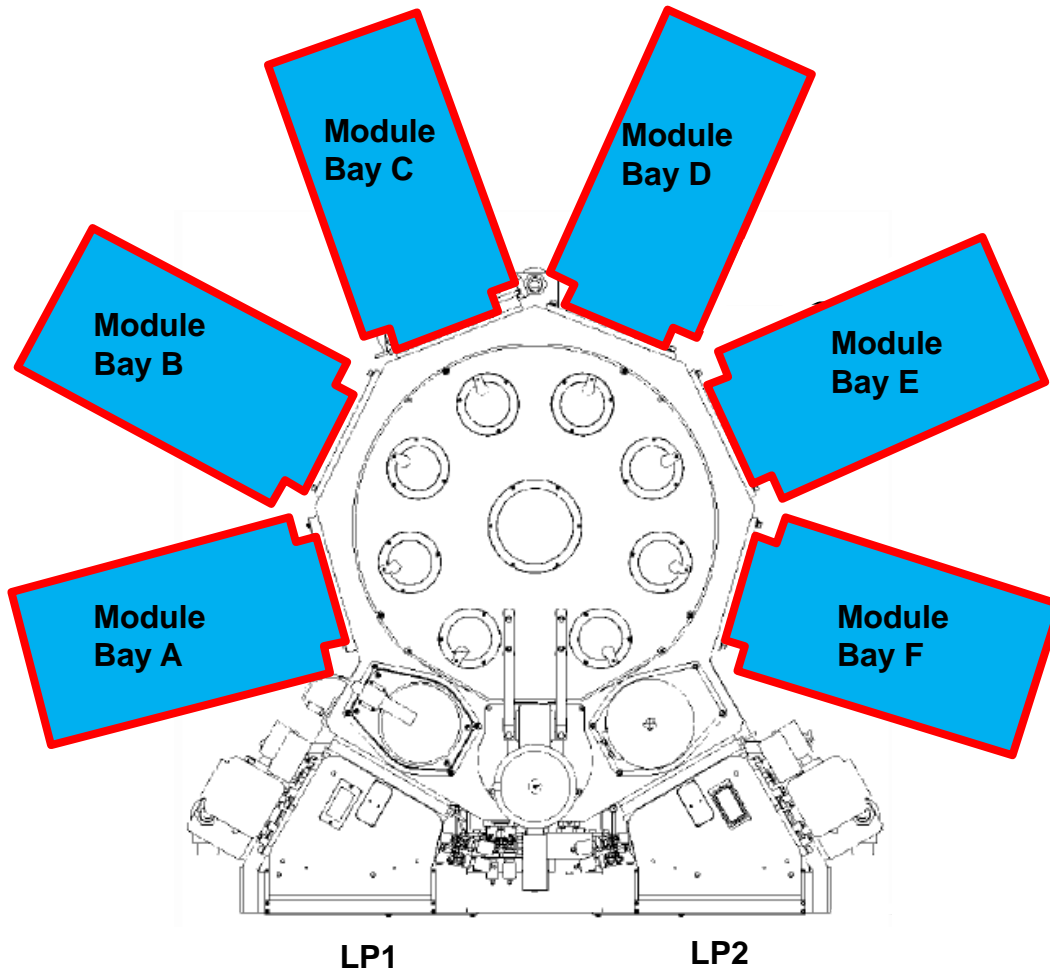
High vacuum encapsulation



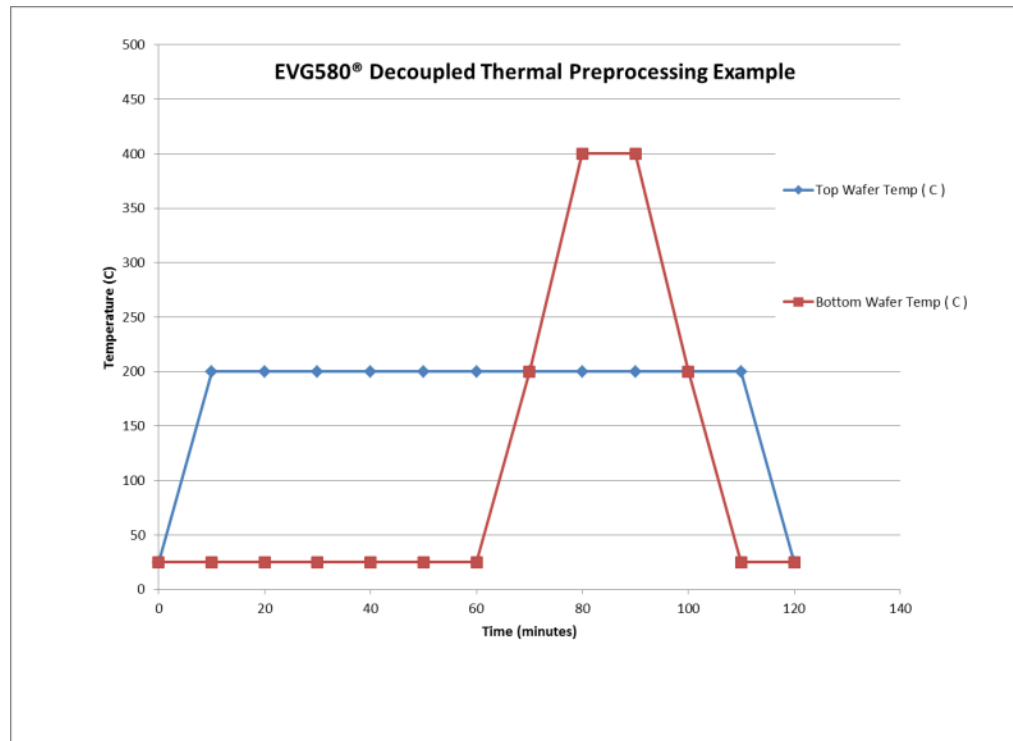
Extracted from Yole roadmap (2013)

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Process Equipment

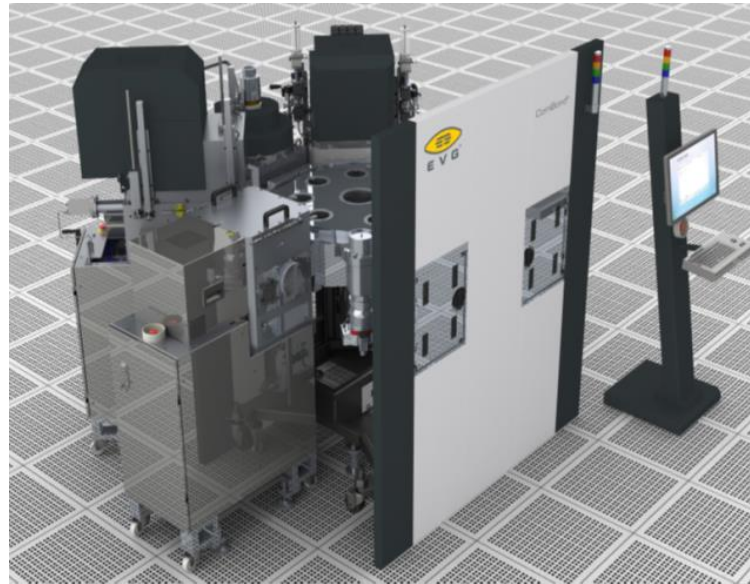
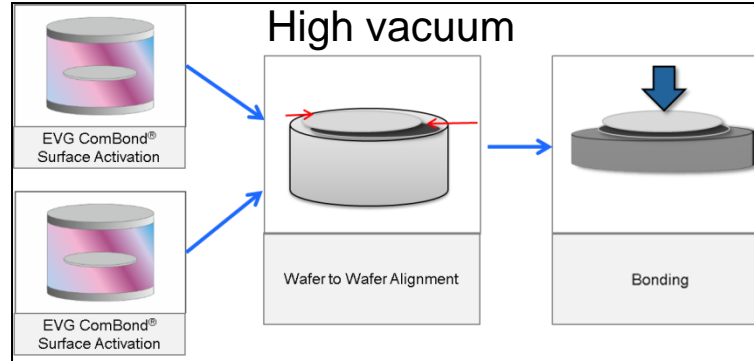


- Improved getter activation due to separate preprocessing of top and bottom wafer.
 - Getter wafer can be activated at a high temperature
 - Other wafer can be baked out at lower temperature if required.



Process Equipment

Used Process Flow and System



EVG ComBond®

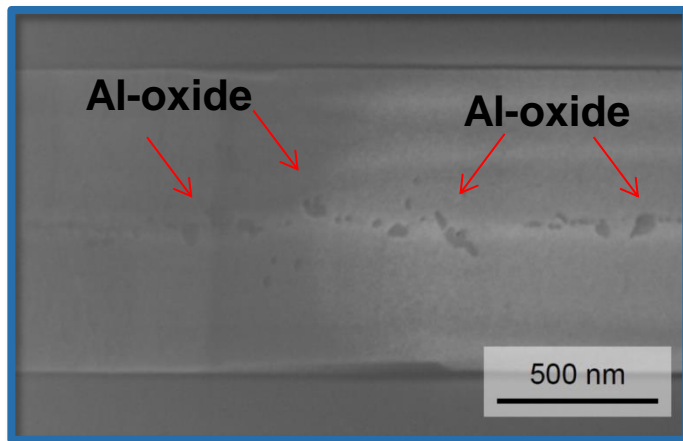
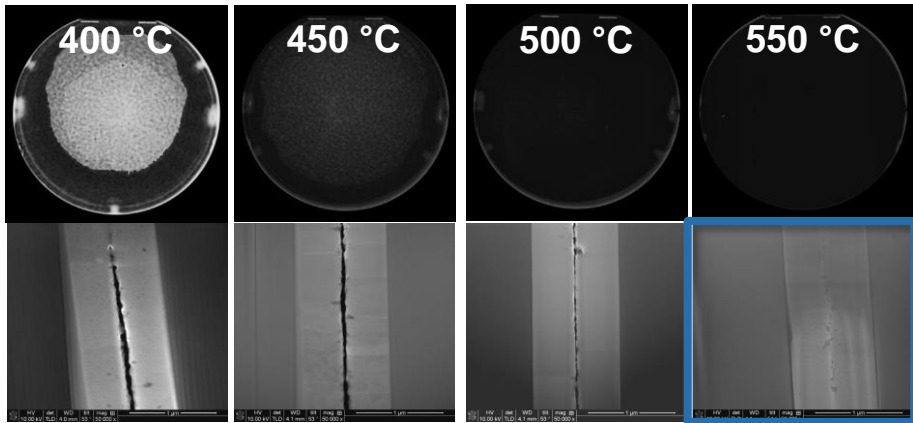
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Process Results

Results – Al-Al

Conventional Bonder (EVG®520IS)

1.9 Mpa, 400-550 °C, 2 h

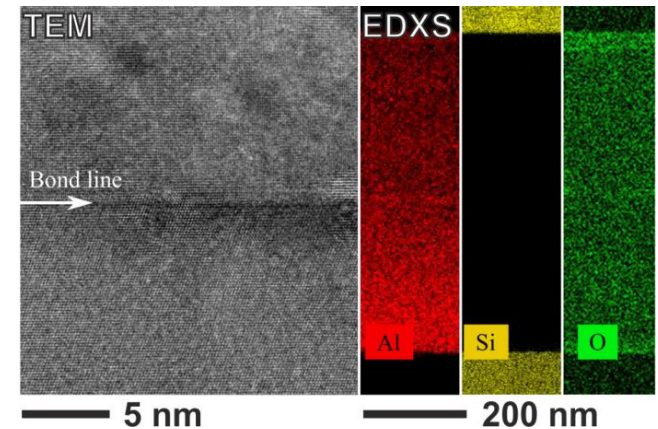
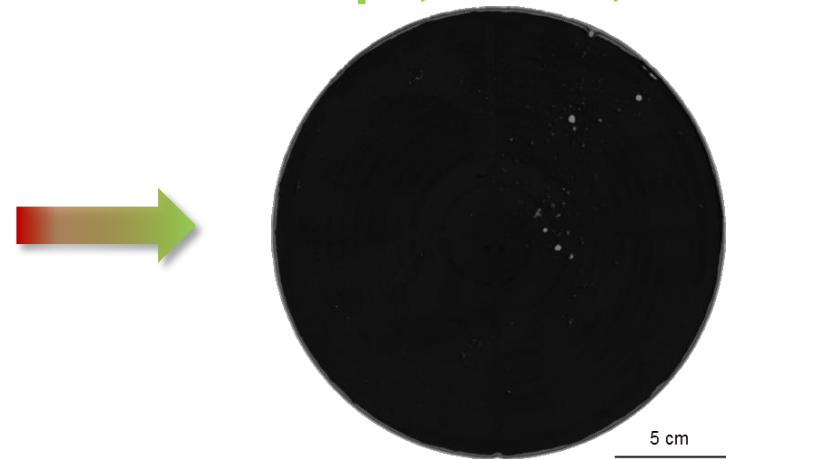


SEM cross-section.

Dragoi et al., *Microsyst Technol*, **18** (7-8), pp 1065–1075 (2012)

EVG ComBond®

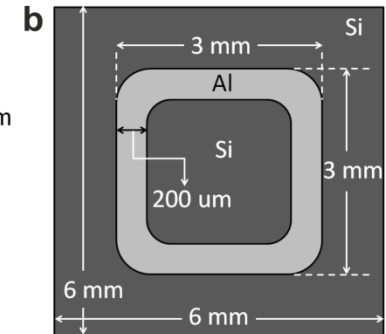
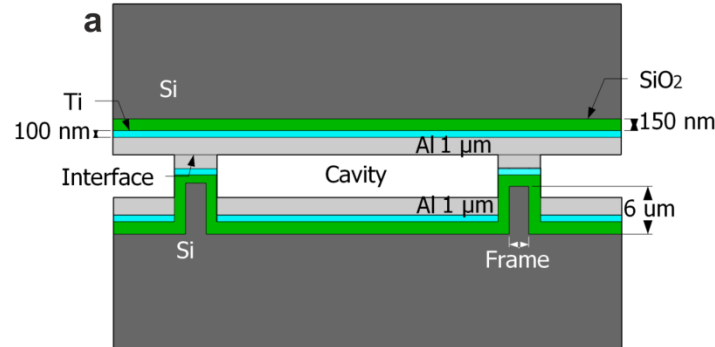
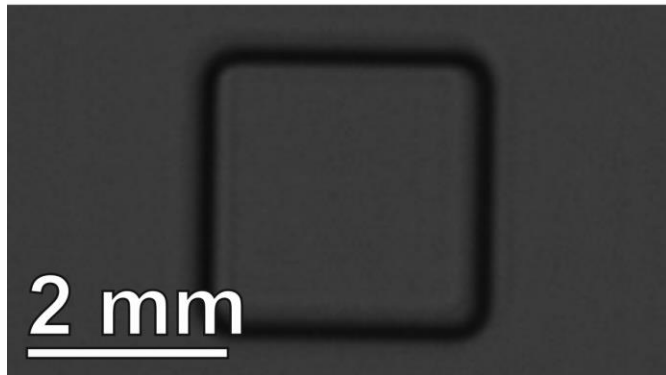
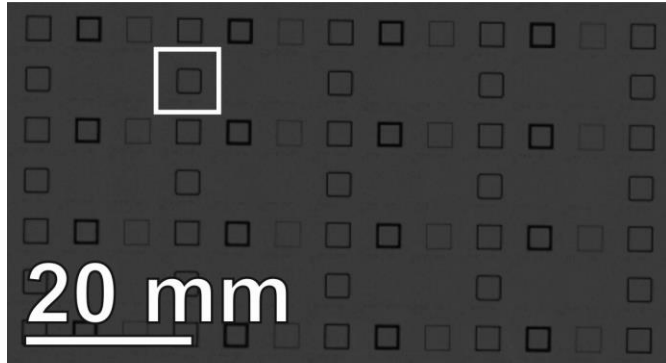
1.9 Mpa, 150 °C, 1.5 h



TEM cross-section.

Hinterreiter et al., «Surface pretreated low-temperature aluminum-aluminum wafer bonding», *Microsyst Technol*, (2017)

Results – Al-Al



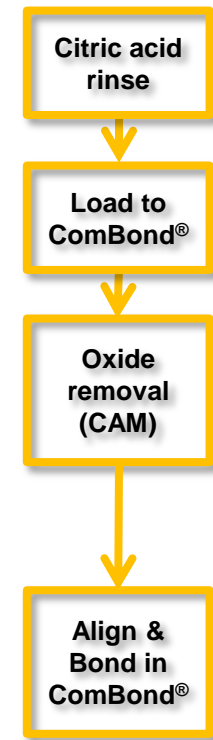
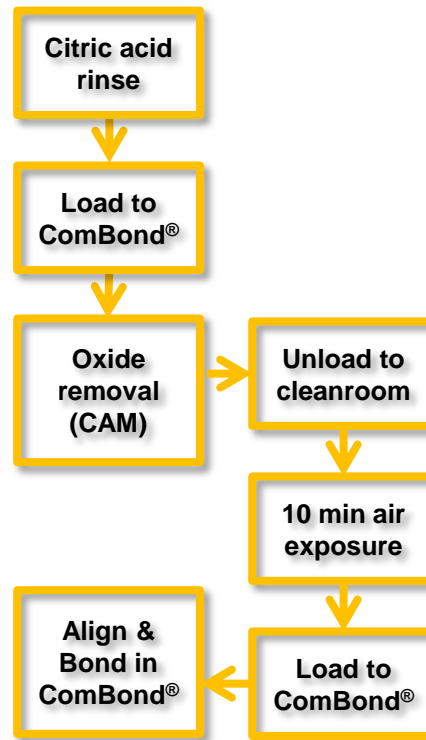
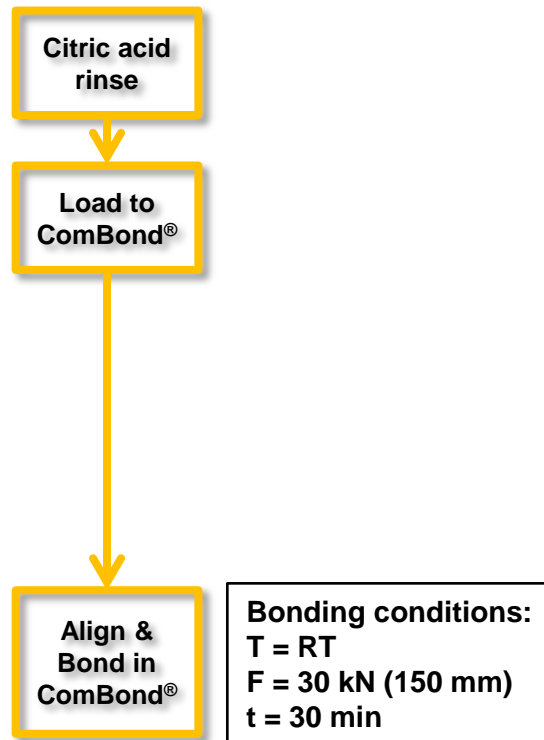
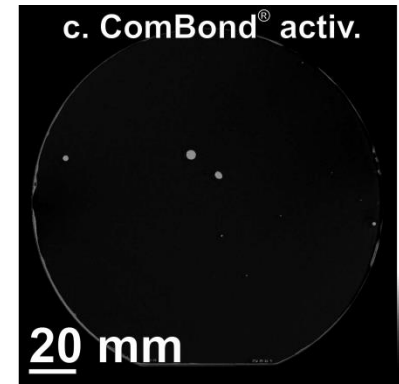
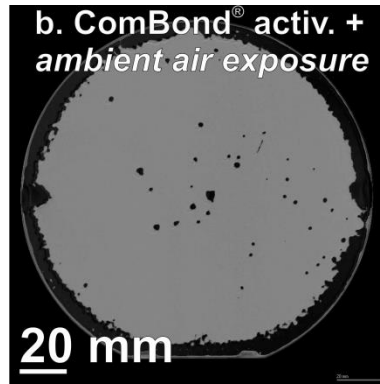
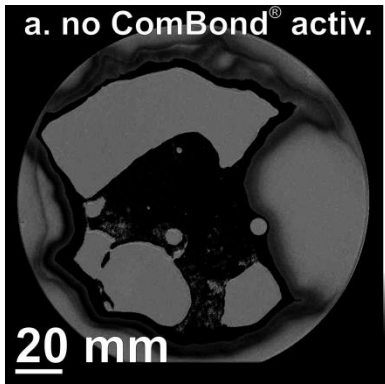
Bonding Temp.	Dicing Yield	Bond Strength	Standard Deviation
100°C	100%	23.13 MPa	9.54 MPa
150°C	100%	36.67 MPa	17.69 MPa

Substrate size = 150 mm diameter; bonding force 60 kN (114 MPa)

C-SAM (left) of Al-Al bonded at 100°C / 1 h: closed frames

Dicing yield and bond strength tests (right) showed high bond strength and 100% dicing yield

Results – Cu-Cu

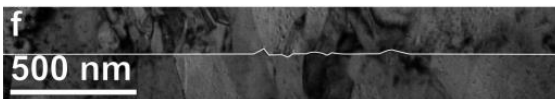
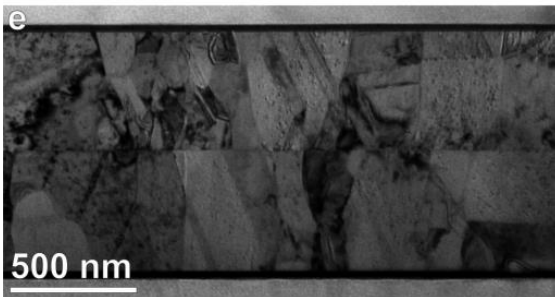


Sample “c”

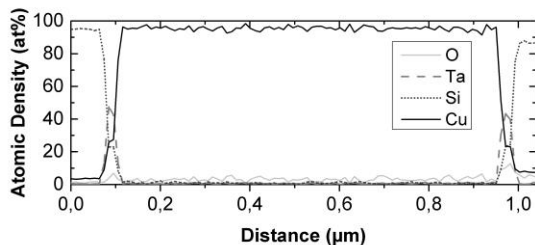
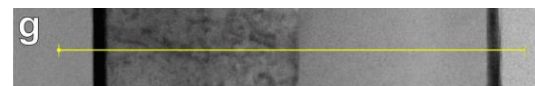
SEM



TEM



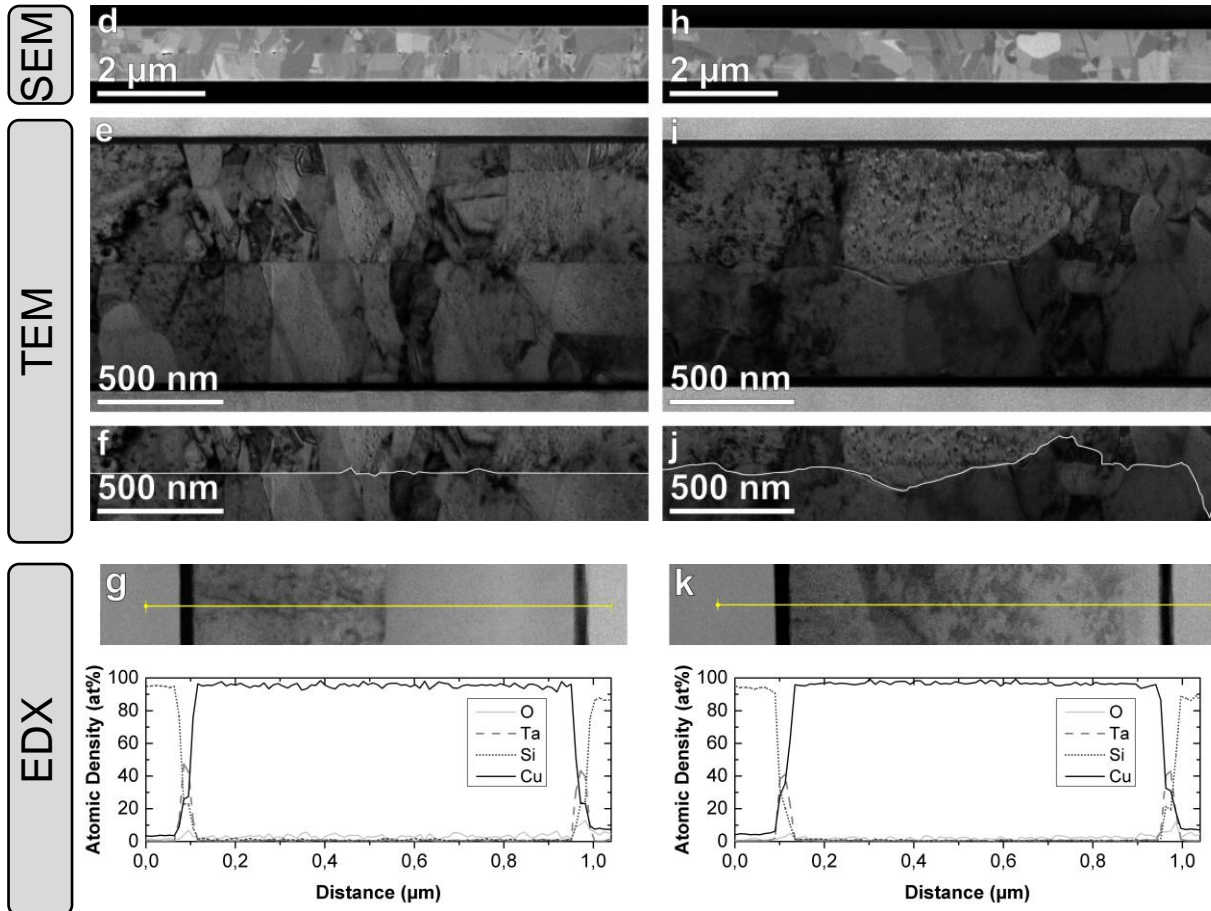
EDX



SEM, TEM & EDX of the sample processed using *in-situ* ComBond® process flow. Cu-Cu bonded at room temperature before (d-g) and after (h-k) annealing @200°C / 5 h: moderate and significant grain growth across the initial bonding interface, respectively; no oxide at/near the initial bonding interface.

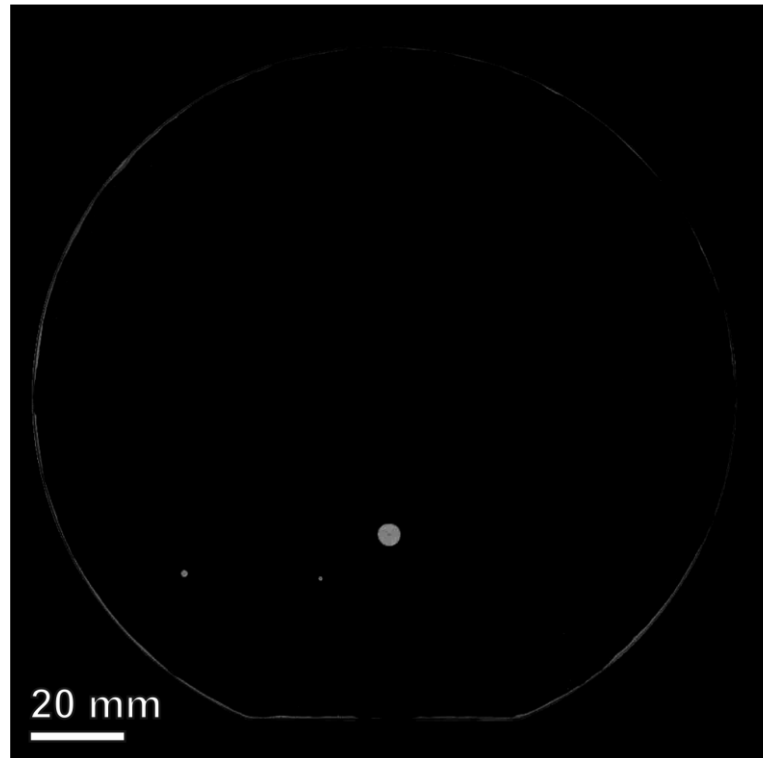
Results – Cu-Cu

By post-bond annealing of sample c



SEM, TEM & EDX of the sample processed using *in-situ* ComBond® process flow. Cu-Cu bonded at room temperature before (d-g) and after (h-k) annealing @200°C / 5 h: moderate and significant grain growth across the initial bonding interface, respectively; no oxide at/near the initial bonding interface.

B. Rebhan and V. Dragoi, Proc. of SPIE Vol. 10246 102461H (2017)



Bonding conditions:
T = RT
F = 30 kN (150 mm)
t = 30 min

Feasibility study of Au-Au bond

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Summary & Conclusions

Conclusions

- The EVG ComBond® system is a modular platform which enables high vacuum MEMS encapsulation.
- As the native oxide is removed *in-situ* it also allows the usage of materials with challenging oxide layers (Al) for metal bonding.
- Successful demonstration of low temperature metal wafer bonding:
 - Oxide-free interfaces
 - Al-Al @ 100°C & 150°C
 - Cu-Cu @ RT
 - Au-Au @ RT
- Low temperature bonding is an enabling technology for materials with CTE mismatch

Thank you!

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