

Embedded functional ceramics in LTCC, Opportunities and challenges in packaging

37th Chemnitz Seminar »Electronic Packaging and Applications«

Qaisar Khushi Muhammad VIA electronic GmbH





- 1. Introduction of KOA Corporation & VIA electronic GmbH (2)
- 2. Low-temperature co-fired ceramic (LTCC) substrate technology (3)
- 3. Embedded functional ceramics into LTCC substrates (1)
 - SAPHIR Project (6)
- 4. VIA footprints on electronic packaging (2)
- 5. Summary (1)



Partnership for Innovation

VIA Electronic GmbH

Production start 1997 Since 01.09.2017 KOA Europe GmbH Main shareholder Hermsdorf, Thüringen

Focused :

- LTCC technology
- Aerospace, defense, automotive, medical, & telecommunications
- RF modules, interposers, sensors, and detectors



KOA Corporation Japan



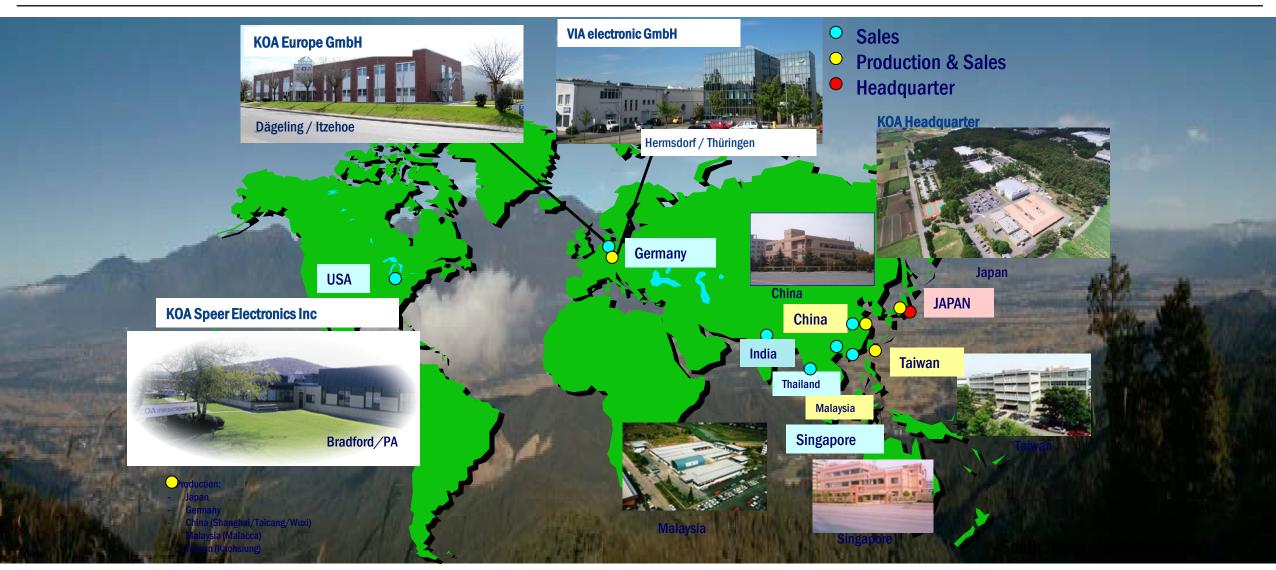
Since 10.03.1940 KOA Corporation Japan 4,144 employees world wide Headquarter: Ina Valley, Nagano Prefecture

Focused :

- Passive Components
- Resistors, capacitors, Inductors, sensors, & modules
- Environment-friendly products

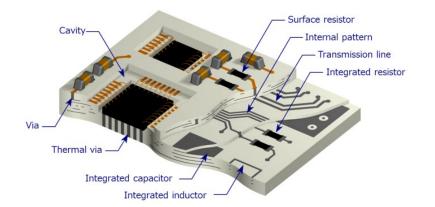


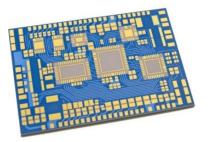
KOA Corporation Worldwide Network

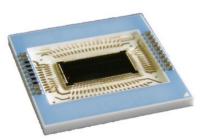




- Multilayer packaging technology with up to 20+ layers
- Moderate firing temperature (below 900 °C)
- Co-firing with highly conductive materials (Ag, Au)
 - Filled via through hole
 - Screen-printed conductor pattern
- Precision cavities and channels
- Thermal vias to enhance heat transport
- Buried components (resistors, capacitors, inductors)



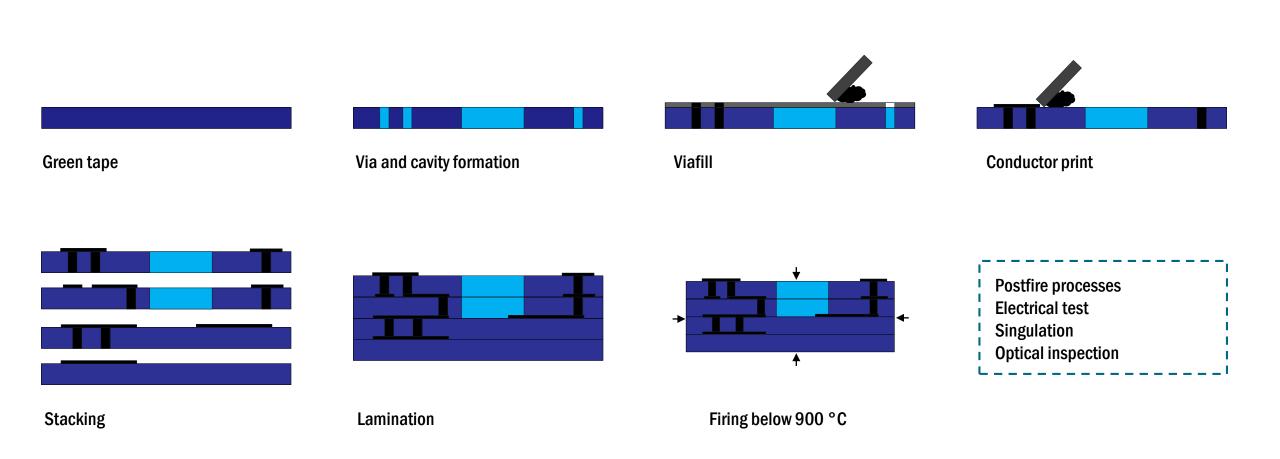




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LTCC technology: Manufacturing process





Packaging possibilites and applications



Interposer and packages

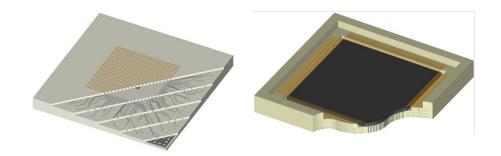
- High density through multilayer structure and fine-line print
- CTE close to \rightarrow Si and GaAs
- Enhanced heat transfer by \rightarrow thermal vias
- High dimensional accuracy and flatness

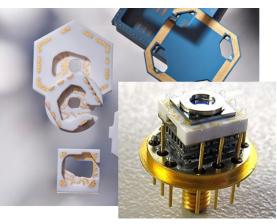
RF modules

- Low-loss ceramic material with a stable dielectric constant
- Low ohm conductors (e.g., Ag)

Harsh environment applications

- Heat and humidity resistance
- Long term stability (no outgassing)
- > Ceramic "PCB" for demanding electronic applications





Sensor carrier and fully integrated detector before hermetic sealing

Magnetic filed detector installed at ITER nuclear fusion reactor

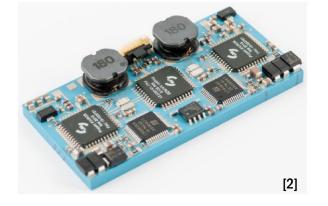


One of the Low-Temperature Co-fired Ceramic (LTCC) sensor prototypes manufactured by Via Electronic



Examples include:

- 1. High-k dielectrics for buried capacitors^[1]
- 2. Ferrites for transformer or circulator applications ^[2]
- 3. NTC materials as temperature sensors ^[3]
- 4.



[1] A.H. Feingold, Materials for capacitive and inductive components integrated with commercially available LTCC Systems. Proc. IMAPS 2003 [2] Projekt KAIROS, Keramische Aufbau- und Integrationstechnik für robuste Signal- und Leistungselektronik

[3] M. Hrovat et al. Thick-film NTC thermistors and LTCC materials: The dependency of the electrical and microstructural characteristics on the firing temperature. J. Eur. Ceram. Soc. 29, 15 (2009) [4] C. Teichmann, J. Töpfer. Sintering and electrical properties of Cu-substituted Zn-Co-Ni-Mn spinel ceramics for NTC thermistors thick films. J Eur. Ceram. Soc. 42, 5 (2022)

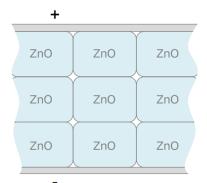
Current topic!

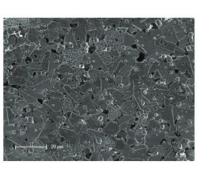
> Integration of varistor ceramics into LTCC substrates

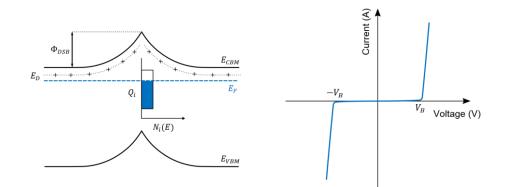
SAPHIR Project: 09/2022 - 08/2025



ZnO-based ceramic varistors

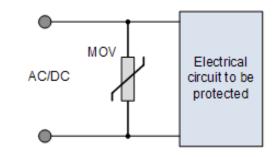






Clarke, David R. "Varistor ceramics." Journal of the American Ceramic Society 82.3 (1999): 485-502.





KOA Chip Varistors (NV73A1H)

Voltage surge protection in electronic circuits

- Breakdown Voltage (V_{BR})
- Nonlinear coefficient Alpha (α)
- Large energy handling capability



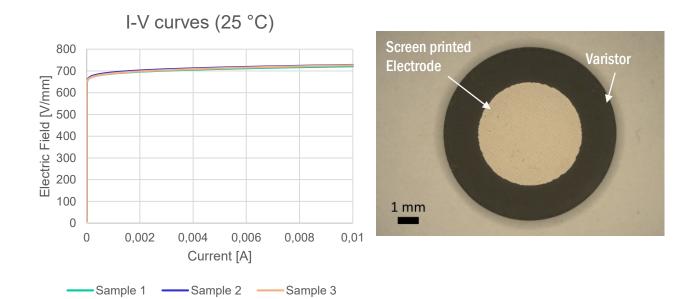
Development tasks

- 1. Reduce the firing temperature of ZnO varistors below 900 °C to allow cofiring with LTCC
- 2. Development of a screen-printable paste
- 3. Printing and cofiring of varistor material with LTCC base material and electrode system



Low-temperature sinterable varistor ceramic

• ZnO+ spinels + BBSZ glass + Additives \rightarrow Disc-shaped samples fired at 900 °C



Parameters	Sample 1	Sample 2	Sample 3
V _{BR}	688 V/mm	693 V/mm	689 V/mm
Alpha (α)	72	66	67

VIA FI FCTRONIC

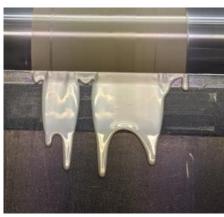
Member of the KOA group

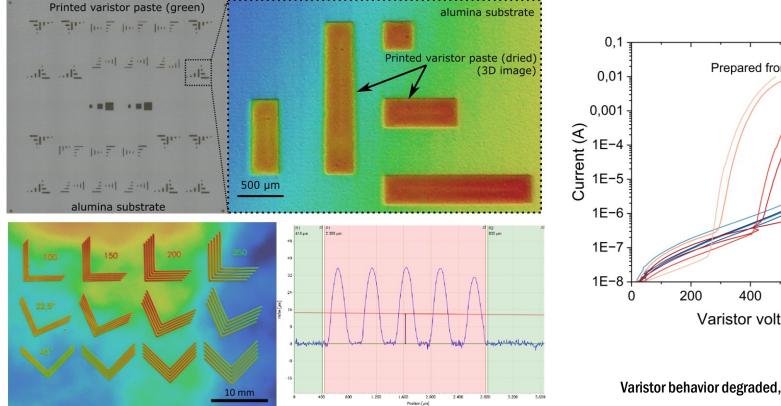
- Low sintering temperature (900 °C)
- Retained Varistor behavior
- Clearly defined breakdown voltage (V_{BR})
- > Non-linear coefficient: Alpha (α) > 50

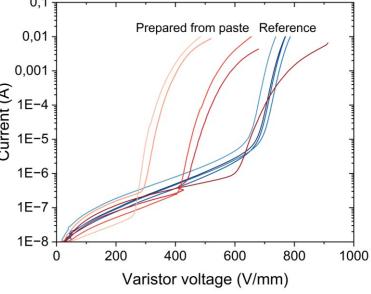


Screen printable Varistor paste and its electrical behavior









Varistor behavior degraded, but clearly demonstrated

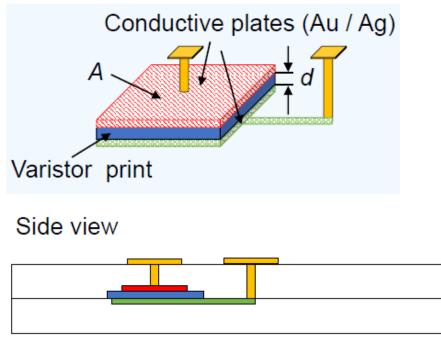
Printing behavior on Alumina



Embedded functional ceramics in LTCC

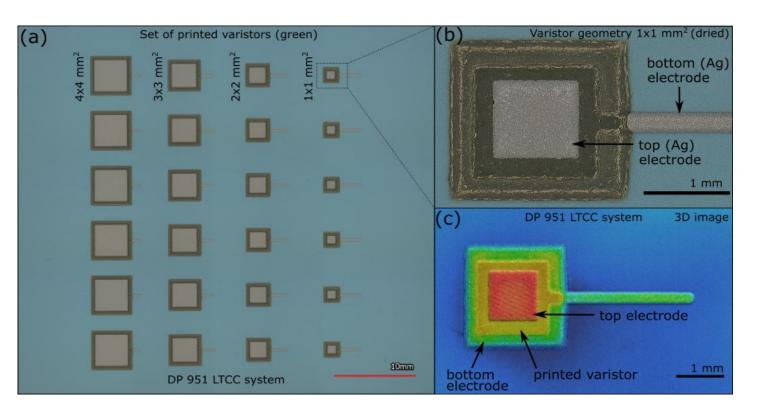


Design and screen printing



2 layers of LTCC DuPont DP 951 PX substrate

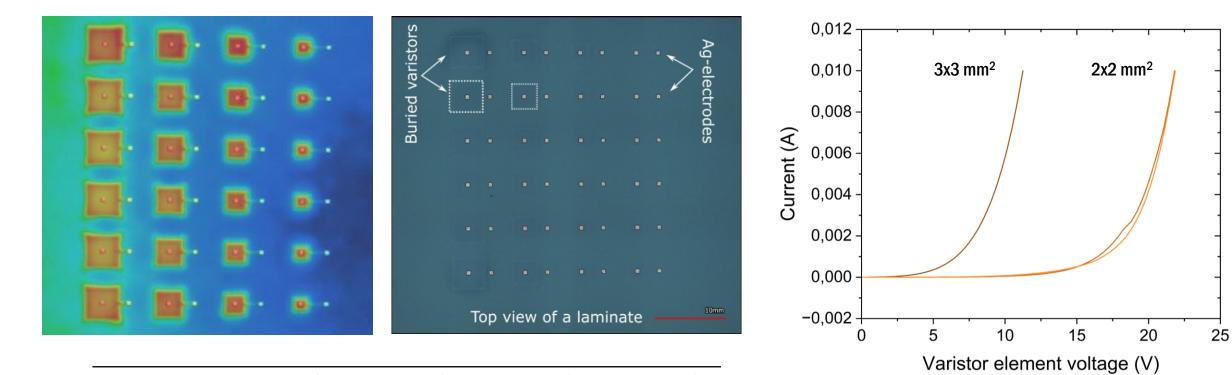
Tested metallization: Ag, Ag/Pd, Au



Printed varistor elements after drying, before lamination

Embedded functional ceramics in LTCC





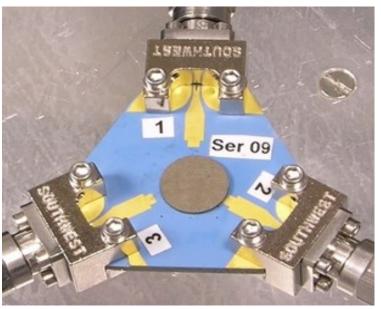
Parameter	Au - 2x2 mm²	Au - 3x3 mm²	Ag - 2x2 mm²	Ag - 3x3 mm²
α _{0.1/1mA}	25.7	24.5	5.0	3.5
I _L [A]	4.7E-06	6.6E-06	4.5E-04	3.3E-04



LTCC technology have more to offer..





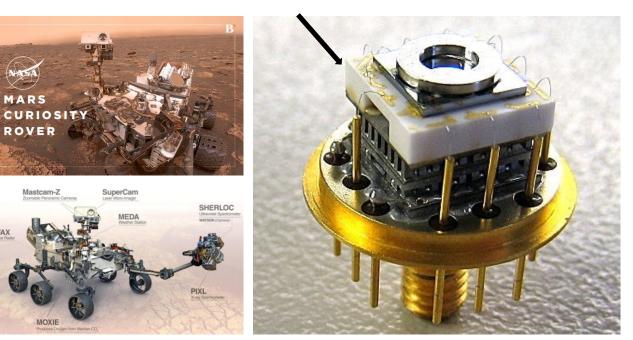


Projekt INFERSAT

- 3D integration technology
- Satellite communication
- Suitable for radar systems

EDX - detector

Mars mission "Curiosity" and "Perseverance"

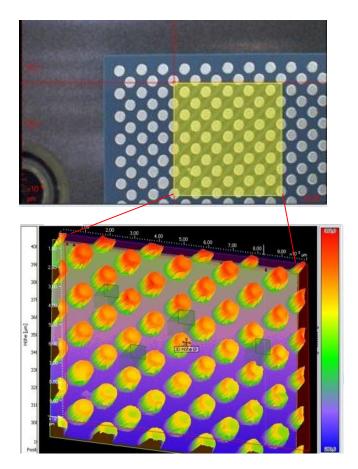


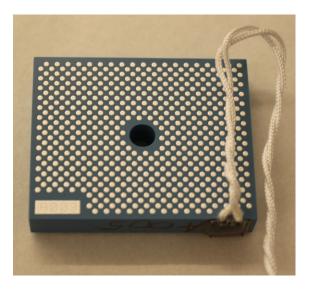
EDX – Sensor carrier for KETEK

LTCC technology have more to offer..

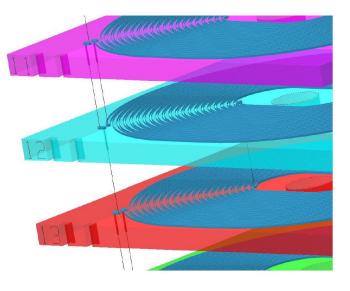


Application: Nuclear Fusion Reactor / magnetic field sensor (ITER project)





- Sensor module with Ag-wire interconnection
- Printed metal dots on top
- Operating temperature 300 400 °C



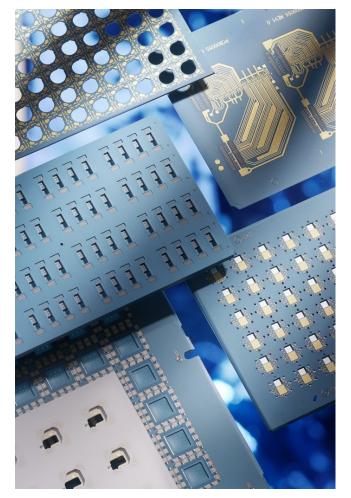
- 38 layers each layer with
- 20 high precision windings
- Connected with electrical vias

... and there are ways for electronic packaging

Member of the KOA group

LTCC technology

- Low permittivity tolerance
- Good thermal conductivity
- Low coefficient of thermal expansion or TCE (adapted to silicon and GaAs)
- Highly suited for multilayer modules
- Integration of cavities and passives such as R-, L-, and C-components



https://via-electronic.de/



Thank you for your attention!

Challenging to realize our 2030 vision

Essential Parts of the World

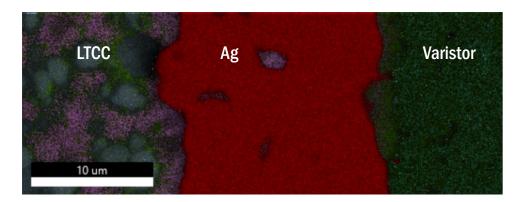


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- http://www.koaeurope.de (EUROPE)
- http://www.via-electronic.de(HERMSDORF, Germany)

Microstructure





EDX superposition of different elements

SEM of varistor shows ZnO grains and most likely the Sb spinel, but no Bi-rich phase

